



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>Applicant:</b>	Kazuo Nishiyama	<b>Atty. Docket No.</b>	075834.00074
<b>Serial No.:</b>	09/ 843,630	<b>Group Art Unit:</b>	2812
<b>Filed:</b>	April 27, 2001	<b>Examiner:</b>	James M. Mitchell
<b>Invention:</b>	CHIP-LIKE ELECTRONIC COMPONENTS, A METHOD OF MANUFACTURING THE SAME, A PSEUDO WAFER THEREFOR AND A METHOD OF MANUFACTURING THEREOF		

## DRAWING AMENDMENT

Commissioner of Patents  
Washington, D.C. 20231

ATTN: Official Draftsman

SIR:

Applicant has attached proposed changes to Figures 9-12. The proposed changes are indicated in red ink. Applicant respectfully requests that the changes be accepted and entered in this application.

Respectfully submitted,

Date: **December 30, 2002**

~~Robert J. Depke~~

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Chicago, Illinois 60603  
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**Attorney for Applicant**

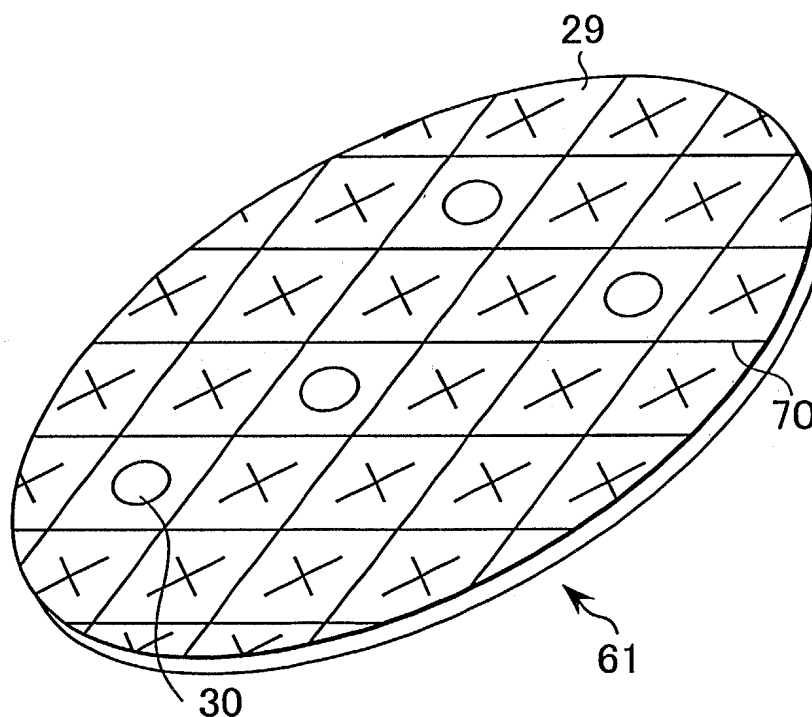
(Reg. #37,607)

CHI1 #199528 v1



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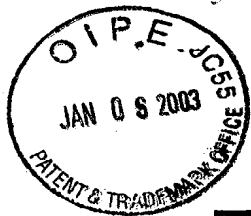
FIG.9



○:NON-DEFECTIVE CHIP

×:DEFECTIVE CHIP

Prior Art



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FIG.10A

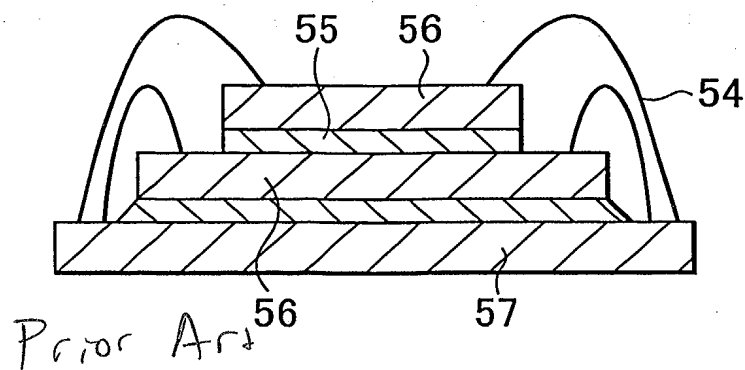


FIG.10B

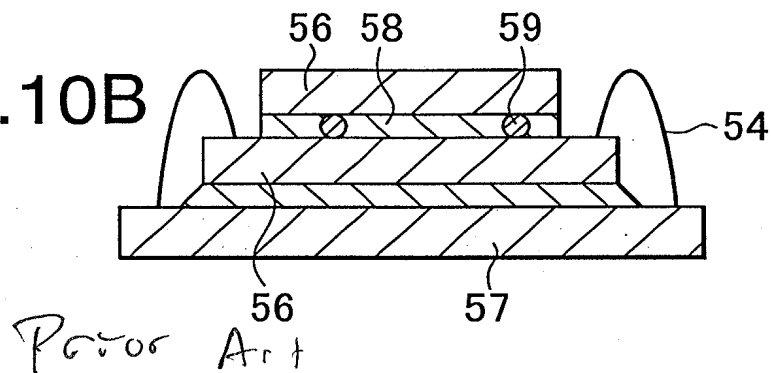
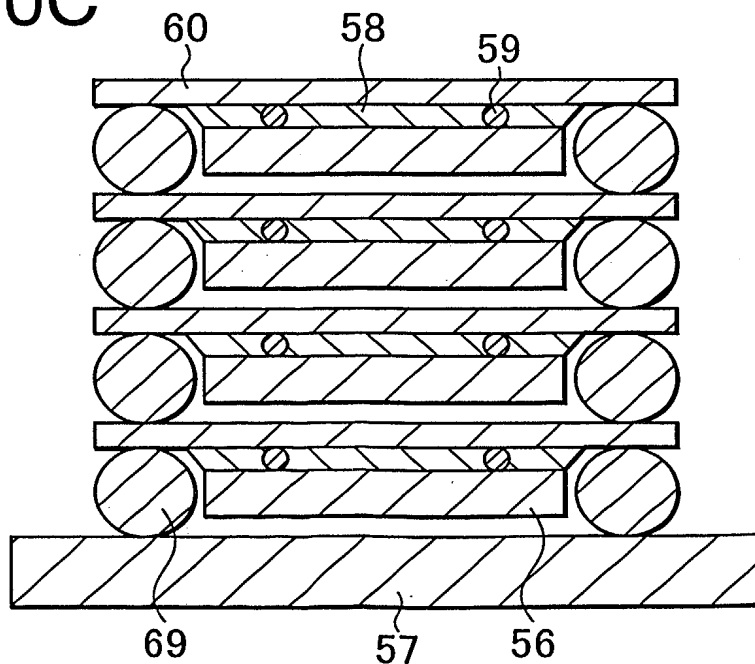


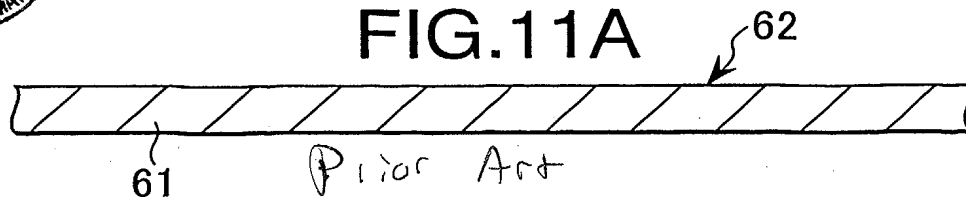
FIG.10C





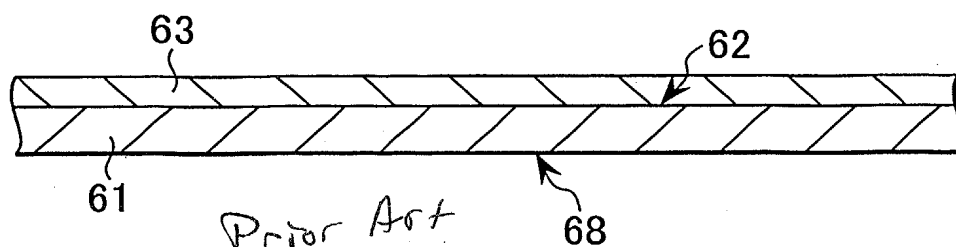
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FIG.11A



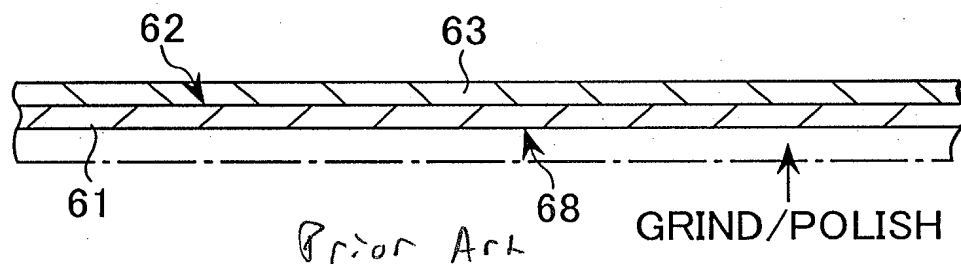
Prior Art

FIG.11B



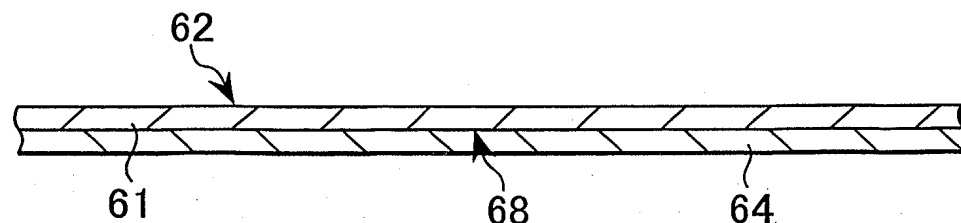
Prior Art

FIG.11C



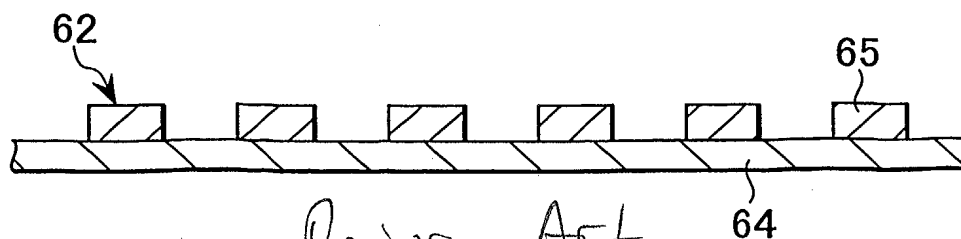
Prior Art

FIG.11D



Prior Art

FIG.11E

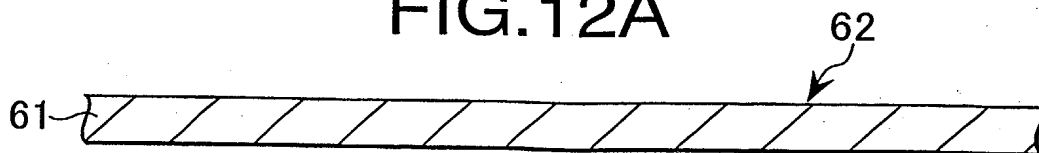


Prior Art



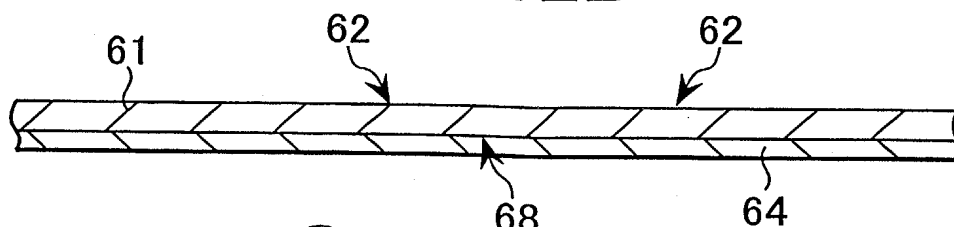
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FIG.12A



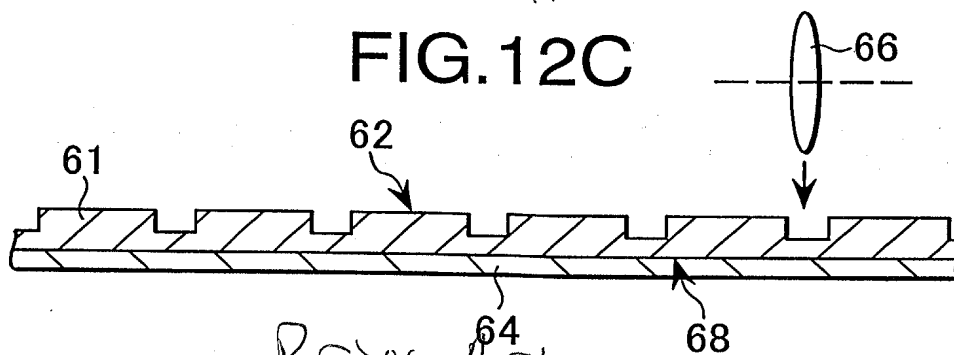
Prior Art

FIG.12B



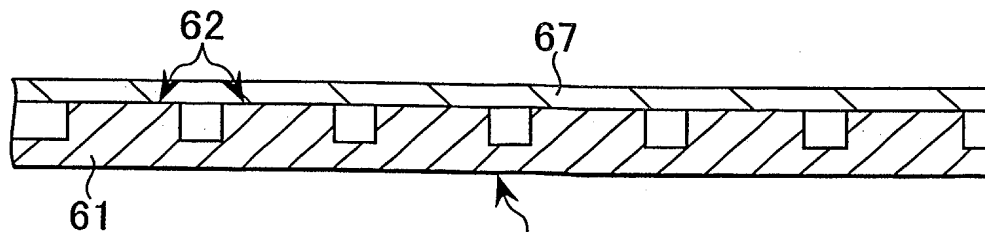
Prior Art

FIG.12C



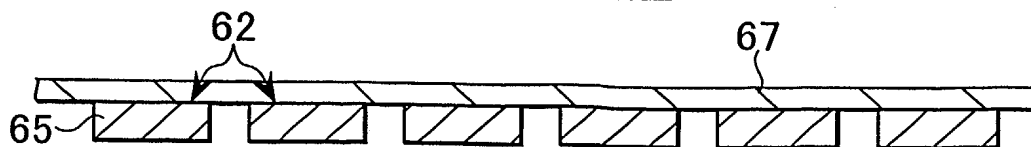
Prior Art

FIG.12D



Prior Art

FIG.12E



Prior Art